

PROFESSIONAL WORK EXPERIENCE:

## R L JALAPPA INSTITUTE OF TECHNOLOGY

(Affilated to Visvesvaraya Technological University, & Approved by AICTE) Doddaballapura - 561203, Bangalore Rural District

## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING.

Designation  Area of Interest	Assistant Professor  Vlsi & Embedded Systems Dsp,	
Email ID	vijaykumaryum@gmail.com vijaykumar@rljit.in	
design and embedded sys	tem), B.E(electronics and communication	M.Tech(vlsi on engineering)
<b>EDUCATION:</b>		
other Discrete Labs (AC,	DC, AEC,)	Matlab, Multisim and
other Discrete Labs (AC,	DC, AEC,)	Matlab, Multisim and

PUBLICATIONS:	
JOURNAL;	
INTERNATIONAL CONFERENCE:	
NATIONAL CONFERENCE:	
BOOK CHAPTERS:	
TEXT BOOK AUTHORED:	
PATENTS:	
FUNDED PROJECTS:	

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FDP/WORKSHOP ATTENDED:	
CONFERENCE/FDP/WORKSHOP CONDUCTED:	
question papers, valuation work, Internal and Exte	Setting of VTU rnal Lab Examiner,
UNIVERSITY RESPONSIBILITIES:	
OTHER RESPONSIBILITES:	
COLLEGE LEVEL:	
DEPARTMENT LEVEL: and time table Coordinator	IETE co- ordinator
IETE	MISTE,
PROFESSIONAL MEMBERSHIPS:	
HONORS/AWARDS:	
OTHER SIGNIFICANT ACHIEVEMENTS (if any):	

Mobile No: 9742778679

**CONTACT DETAILS:**