

RESUME

Objective

- To utilize my education, capabilities, creative ideas, with a self-motivated and positive approach within an organization that will help my professional and personal skills.
- Willing to give 100% efforts to the realization of goals at specified targets.

Dr.VARALAKSHMI K R

Email

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Present Address

Prabhakar N, #23 rd cross,
Dargajogalli
Shanti Nagar
Doddaballapur
Bangalore-561 203
Karnataka, India

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Personal Data

Date of Birth : 14th Feb 1986
Gender : Female
Husband Name : Prabhakar N
Nationality : Indian
Marital Status : Married
Languages Known: English,
Kannada,
Telugu.

Qualification

- **M.Tech (Digital Communication & N/W), SJCIT , VTU.**
- **B.E.(ECE) from NCET ,Bangalore(VTU university).**

Software Skills

- NS – 2
- Embedded Systems.
- MATLAB, Keil, Multisim
- Xilinx

Primary Asset:

- Quick learning ability.
- Positive and open-minded approach.
- Ability to work with a team.

Professional Experience of 13 year's

MS Engineering College

- **Designation** : Lecturer.
- **Tenure** : Aug 2008 to Nov 2010
- **Subjects & Lab Handled** : Basic Electronics, Logic Design, Wireless Networks, Programming in C++, HRM, Microcontroller (8051), Electronics and Logic Design Lab ,Microprocessor(8086) Lab.

RL Jalappa Institute of Technology

- **Designation** : Lecturer.
- **Tenure** : Jan 2011 to July 2011
- **Subjects & Lab Handled** : Microcontroller (8051) Theory & Lab.

Sai Vidya Institute of Techonology

- **Designation** : Asst. Professor.
- **Tenure** : Aug 2013 to Aug 2015
- **Subjects & Lab Handled** : Basic Electronics, Microcontroller (8051), Logic Design Lab, Microcontroller Lab, DSP Theory & Lab.
- **Worked as a NBA Coordinator for Criteria-4**

RL Jalappa Institute of Technology

- **Designation** : Assistant Professor
- **Tenure** : Aug 2015 to 30-08-2022
- **Subjects & Lab Handled** : Microcontroller (8051) ,MSP430 Theory & Lab, Basic Electronics, Basic electrical Engineering,Logic Design Lab, RTS, OS, MEMS, Computer Networks, Embedded lab.

PRESIDENCY UNIVERSITY

- **Designation** : Assistant Professor
- **Tenure** : Sep 2022 to 30-09-2024

ACADEMIC RECORD

Master of Technology(Digital communication networks(DCN))

- **College** : SJCIT
- **Year of Passing** : 2013
- **Board** : Visvesvaraya Technological University (VTU)
- **Percentage** : 75% .

Bachelor of Engineering (Electronics & communication Engineering)

- **College** : NCET (Year of Passing : 2008)
- **University** : Visvesvaraya Technological University (VTU)
- **Percentage** : 65.00%

Extra Curricular Activities

- **Published 4 International Journals**, Participated in more than 30 Workshops and Faculty Development programs and Joint Secretary for College level Events.
- **Project Coordinator:** Guided many students with their mini project and final year project activities.(UG&PG).
- **Worked as a Department NBA Coordinator for Criteria-4.**
- **Worked as a Department NAAC Coordinator for Criteria-6.**

PUBLICATION DETAILS

1.Published paper on“**Adaptive PSO- Based Power Scheduling Algorithm in Wireless Mesh Network**”: Computer Integrated Manufacturing System" Jisuanji jicheng Zhizao Xitong ,ISSN: 1006-5911| Issue:- Regular,Q2.

2.Published paper on “**Adaptive Power and Rate link Scheduling in Wireless Mesh Networks.**”, in JARDCS journal, Volume 10,Issue 9,JULY 2018,Pages:141-148,ISSN:1943-023x.

3.Published paper on “**A HIGH PERFORMANCE NETWORK –ON-CHIP SYSTEM WITH PATH –CONGESTION-AWARE ADAPTIVE ROUTING** ”,in IJSET journal, Volume 3,Issue 3,JUNE 2015 with IMPACT FACTOR -3.25,ISSN:2348-4098.

4.Published paper on “**LOCALIZATION USING FILTERED DGPS**” ,in IJRET journal, Volume 4,Issue 4,April 2015 with IMPACT FACTOR -2.375,e-ISSN:2319-1163,p-ISSN:2321-7308.

Publications in Conference Proceedings

1. Presented a paper entitled “**A Modified Secure and Distributed Protocol For Wireless Sensor Networks**”, In National Conference on Computing Technologies and Mechanical Engineering held on 5th and 6th May 2015 at ICEAS, Bangalore.
2. Presented paper on “**A Robust Distributive Approach In Wireless Networks**” in the “National Level Technical Conference” held at RLJIT, Doddabalapur, Bangalore.
3. Presented paper on “**A Cryptographic Algorithm based on Linear Feedback shift Register**”, in National conference on Emerging trends in Electronics & Communication Engineering held on 22nd April 2016 at RLJIT in association with IETE, Bangalore.

Book chapters

https://ciir.in/doc/publication/Detection%20and%20Monitoring%20System_978-81-962230-3-8.pdf#page=19
DETECTION AND MONITORING SYSTEM

Declaration

I hereby declare that above information is true to the best of my knowledge and understanding.

Varalakshmi K R



Place: Doddaballapur

Date: