

R L JALAPPA INSTITUTE OF TECHNOLOGY

(Affilated to Visvesvaraya Technological University, & Approved by AICTE)

Doddaballapura – 561 203, Bangalore Rural District

Department of CS&E (DATA SCIENCE)

Faculty Name	PRANAVA BHARGAVI V	
Designation	Assistant Professor	
Area of Interest	Networking, Digital Design, IoT, Image Processing, Embedded Systems, communiction systems.	(3.6)
Email ID	pranavabhargavi@rljit.in	

EDUCATION:

- Pursuing **Ph.D** in Annamalai University, Chidambaram.
- Post Graduation in Digital Electronics and Communication Systems (M.Tech) from SVCET, Chittoor (affiliated to the J.N.T.U, Anantapur) Obtained a percentage of 82.3-Distinction-during (2011-2013).
- ☐ Graduation in ECE (B.Tech) from KEC, Kuppam (affiliated to the J.N.T.U, Anantapur) Obtained a percentage of 75.6- Distinction during (2006-2010).
- ☐ Intermediate from Sree Venkateswara Junior College, Chittoor (Dt.) (affiliated to the Board of Intermediate Education, Andhra Pradesh). Obtained a percentage of 86.8- Distinction -during (2004-2006).
- Schooling from S.V.S.High School, nallamgadu (affiliated to the S.S.C Board of Secondary Education, Andhra Pradesh). Obtained a percentage of 88.9- Distinction.-in 2004

TECHNICAL SKILLS:

Operating Systems Win-XP,Win-7

Application Software MATLAB, VHDL, CC Studio

Computer packages MS Office
Programming Languages C,VHDL,HDL

PROFESSIONAL WORK EXPERIENCE:

• June 2015 to Till

Working as Assistant Professor at Sri Venkateswara College of Engineering & Technology, Chittoor.

• September 2013 to May 2015

Worked as Assistant Professor at MJR College of Engineering & Technology, Piler.

• July 2010 to October 2011

Worked as Assistant Professor at MJR College of Engineering & Technology, Piler.

FDP/WORKSHOP ATTENDED:

- Participated in the **NPTEL** workshop condutced on 12th september 2018 by **IIT MADRAS**, at Kuppam Engineering College, Kuppam.
- Participated in the A.I.C.T.E sponsored seminar on "ADVANCED DIGITAL IMAGE PROCESSING
 TECHNIQUES (ADIPT) held during december 15th-16th, 2017 in the department of ECE, NBKR Institute of
 Science & Technology, Vidyanagar, S.P.S.R. Nellore.
- Participated in DST sponsored one day workshop on "INTEGRATED FARMING AND MUSHROOM CULTIVATION" on 6th October, 2016.
- Participated in TEQIP-II sponsored one week national workshop on "EMERGING TRENDS IN VLSI & SIGNAL PROCESSING" during 22-27 August, 2016.
- Participated in the training programme on "BIG DATA ANALYTICS" from 11th June to 18th June 2016 organized by the department of ECE sponsored by department of Science & Technology.
- Participated in the faculty development programme on "INSTRUCTIONAL DESIGN AND DELIVERY" conducted by S.V.C.E.T at chittoor from 09th June to 10th June 2016.
- Participated in a Workshop on "Advances In VLSI Design and Embedded Systems".
- Participated in project expo in MEGATRON -09 A National Level Students technical symposium held on 13th Feb 2009.

Projects Guided:

Titles:

- 1.Gas Leakage and Fire Detection by Using GPS AND GSM
- 2. Vehicle Theft Control by Using GPS and GSM
- 3. Bank Locker Security System Using RFID And GSM.
- 4. A wireless Surveillance & Safety System for Mine workers based on ZIGBEE.
- 5. Design and Simulation of UART Serial Communication Module Using VLSI.
- 6. ZIGBEE Electic Vehicle Charging System.

Papers Published in Journals:

- Published a Paper on "Power Constrained Contrast Enhancement Based on Histogram Equalization "in IJERT.
- Published a Paper on"Classification of a MRI Brain Image Genetic Algorithm for KNN classifier" in Indian Journal of Public Health Research & Development, October 2018, Vol.9, No. 10.
- Published a Paper on"The Digital Audio Watermarking Based on Tetrolet Wavelet Transform" in Indian Journal of Public Health Research & Development, October 2018, Vol. 9
- Published a Paper on"Trust and Mobility Based Clone Attack Detection in Mobile Wireless Network" in Indian Journal of Public Health Research & Development, October

OTHER RESPONSIBILITES:

DEPARTMENT LEVEL: NIRF Coordinator, CIE Coordinator, Timetable Coordinator.

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