



Sri Devaraj Urs Educational Trust (R)

R.L. JALAPPA INSTITUTE OF TECHNOLOGY

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DODDABALLAPUR - 561 203. BENGALURU RURAL DISTRICT, KARNATAKA.



Department of Electronics & Communication Engineering		
Course Outcome Statements of AY-(2022-23)		
Course Outcome Statement		
Course:	Code: 21MAT 31 Course Name: Transform Calculus, Fourier Series And Numerical Techniques	Faculty: Mrs. Meenakshi H, Mr. Madhu N R,
	Statement: On the completion of the course, the students will be able to:	
Course 231.1	To solve ordinary differential equations using Laplace transform.	
Course 231.2	Demonstrate the Fourier series to study the behavior of periodic functions and their applications in system communications, digital signal processing and field theory.	
Course 231.3	To use Fourier transforms to analyze problems involving continuous-time signals and to apply Z-Transform techniques to solve difference equations	
Course 231.4	To solve mathematical models represented by initial or boundary value problems involving partial differential equations	
Course 231.5	Determine the extremals of functionals using calculus of variations and solve problems arising in dynamics of rigid bodies and vibrational analysis.	
Course Outcome Statement		
Course:	Code: 21EC32 Course Name: Digital System Design Using Verilog	Faculty: Mr. Nandish N
	Statement: On the completion of the course, the students will be able to:	
Course 232.1	Simplify Boolean functions using K-map and Quine-McCluskey minimization technique.	
Course 232.2	Analyze and design for combinational logic circuits.	
Course 232.3	Analyze the concepts of Flip Flops (SR, D, T and JK) and to design the synchronous sequential circuits using Flip Flops.	
Course 232.4	Model Combinational circuits (adders, subtractors, multiplexers) and sequential circuits using Verilog descriptions	
Course Outcome Statement		
Course:	Code: 21EC33 Course Name: Basic Signal Processing	Faculty: Dr. Madhuchandra G
	Statement: On the completion of the course, the students will be able to:	
Course 233.1	Interpret the basics of Linear Algebra	
Course	Interpret Eigen values and Diagonalization of a Matrix, Special Matrices	



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233.2	
Course 233.3	Analyze different types of signals and systems
Course 233.4	Analyze the properties of discrete-time signals & systems
Course 233.5	Analyze discrete time signals & systems using Z transforms

Course Outcome Statement

Course:	Code: 21EC34 Course Name: Analog Electronic Circuits	Faculty: Mr. Manjesh N
	Statement: On the completion of the course, the students will be able to:	
Course 234.1	Interpret the characteristics of BJTs and FETs for switching and amplifier circuits.	
Course 234.2	Design and analyze FET amplifiers and oscillators with different circuit configurations and biasing conditions.	
Course 234.3	Compare the feedback topologies and approximations in the design of amplifiers and oscillators.	
Course 234.4	Design of circuits using linear ICs for wide range applications such as ADC, DAC, filters and timers.	
Course 234.5	Interpret the power electronic device components and its functions for basic power electronic circuits.	

Course Outcome Statement

Course:	Code: 21ECL35 Course Name: Analog and Digital Electronics Lab	Faculty: Dr. Harish S, Mr. Lokesh R, Mr. Manjesh N
	Statement: On the completion of the course, the students will be able to:	
Course 235.1	Design and analyze the BJT/FET amplifier and oscillator circuits.	
Course 235.2	Design and test Opamp circuits to realize the mathematical computations, DAC and precision rectifiers.	
Course 235.3	Design and test the combinational logic circuits for the given specifications.	
Course 235.4	Test the sequential logic circuits for the given functionality.	
Course 235.5	Demonstrate the basic electronic circuit experiments using SCR and 555 timers.	

Course Outcome Statement

Course:	Code: 21EC381 Course Name: LD (Logic Design) Lab using Pspice / MultiSIM	Faculty: Miss. Aarathi V
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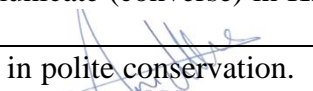
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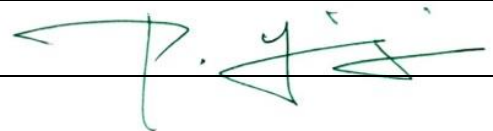
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Statement: On the completion of the course, the students will be able to:	
Course 236.1	Build and test circuits using power electronic devices
Course 236.2	Analyze and design-controlled rectifier, DC to DC converters, DC to AC inverters and SMPS.
Course 236.3	Analyze instrument characteristics and errors in measurement
Course 236.4	Explain circuits for multi range Ammeters, Voltmeters and Bridges to measure passive component values and frequency.
Course 236.5	Analyze the operation of Digital instruments, transducers, PLCs and Use Instrumentation amplifier for measuring physical parameters
Course Outcome Statement	

Course:	Code: 21KSK37/47	Faculty: Ms Malini
	Course Name: Samskrutika Kannada	
Statement: On the completion of the course, the students will be able to:		
Course 238.1	1. ಕನ್ನಡ ಭಾಷೆ, ಸಾಹಿತ್ಯ ಮತ್ತು ಕನ್ನಡದ ಸಂಸ್ಕೃತಿಯ ಪರಿಚಯವಾಗುತ್ತದೆ.	
Course 238.2	2. ಕನ್ನಡ ಸಾಹಿತ್ಯದ ಆಧುನಿಕ ಪೂರ್ವ ಮತ್ತು ಆಧುನಿಕ ಕಾವ್ಯಗಳು ಮತ್ತು ಸಂಸ್ಕೃತಿಯ ಬಗ್ಗೆ ಆಸಕ್ತಿಯು ಮೂಡುತ್ತದೆ.	
Course 238.3	3. ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿಗಳ ಪರಿಚಯವಾಗುತ್ತದೆ.	
Course 238.4	4. ಕನ್ನಡ ಭಾಷಾಭ್ಯಾಸ, ಸಾಮಾನ್ಯ ಕನ್ನಡ ಹಾಗೂ ಆಡಳಿತ ಕನ್ನಡದ ಪದಗಳ ಪರಿಚಯವಾಗುತ್ತದೆ.	
Course Outcome Statement		
Course:	Code: 21KSK37/47	Faculty:
	Course Name: Balake Kannada	
Statement: On the completion of the course, the students will be able to:		
Course 238.1	Learn local language for comfortable life.	
Course 238.2	Listen and understand the Kannada language properly.	
Course 238.3	Speak, read and write Kannada language as per requirement.	
Course 238.4	Communicate (converse) in Kannada language in their daily life with kannada speakers.	
Course 238.5	Speak in polite conversation.	


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Course Outcome Statement	
Course:	Code: 21CIP37/47 Course Name: Constitution of India and Professional Ethics
	Faculty:
	Statement: On the completion of the course, the students will be able to:
Course 238.1	Know about the basic structure of Indian Constitution.
Course 238.2	Know the Fundamental Rights (FR's), DPSP's and Fundamental Duties (FD's) of our constitution.
Course 238.3	Know about our Union Government, political structure & codes, procedures.
Course 238.4	Know the State Executive & Elections system of India.
Course 238.5	Learn the Amendments and Emergency Provisions, other important provisions given by the constitution.
Course Outcome Statement	
Course:	Code: 21EC41 Code: Course Name: Maths for Communication Engineers
	Faculty: Mr. Madhu N R, Mrs. Aruna
	Statement: On the completion of the course, the students will be able to:
Course 241.1	Recall the basic laws and definitions (with mathematical representations) in Electric and Magnetic fields.
Course 241.2	Apply the basic laws of Electric and Magnetic fields to arrive at Divergence Theorem, Current continuity Equation, Curl, Stokes' theorem.
Course 241.3	Apply Electric and Magnetic field concepts to arrive at Maxwell's equations, Electromagnetic wave equations and Poynting's theorem (Important concepts related to Communication link).
Course 241.4	Recall the definitions related to Random variables and Random Processes.
Course 241.5	Model the Random events in the Communication set-up and determine useful statistical parameters.
Course Outcome Statement	
Course:	Code: 21EC42 Course Name: Digital Signal Processing
	Faculty: Dr Madhuchandra G, Mis. Aarthi V
	Statement: On the completion of the course, the students will be able to:
Course 242.1	Determine response of LTI systems using time domain and DFT techniques
Course 242.2	Compute DFT of real and complex discrete time signals

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Course 242.3	Compute DFT using FFT algorithms	
Course 242.4	Design FIR and IIR Digital Filters	
Course 242.5	Design of Digital Filters using DSP processor	
	Course Outcome Statement	
Course:	Code: 21EC43 Course Name: Circuits & Controls	Faculty: Mr. Lokesh R, Mr. Manjesh N
	Statement: On the completion of the course, the students will be able to:	
Course 243.1	Analyze and solve Electric circuit, by applying, loop analysis, Nodal analysis and by applying network Theorems.	
Course 243.2	Evaluate two port parameters of a network and apply Laplace transforms to solve electric networks.	
Course 243.3	Develop the mathematical model and transfer function for a given electrical systems	
Course 243.4	Determine the time domain specifications for 1 st and 2 nd order systems and determine the stability of a system using Routh's criterion.	
Course 243.5	Determine the stability of a system using root locus technique and Bode plots, represent state model and find the time response of the system.	
	Course Outcome Statement	
Course:	Code: 21EC44 Course Name: Communication Theory	Faculty: Dr. Anilkumar C, Dr. Harish S
	Statement: On the completion of the course, the students will be able to:	
Course 244.1	Interpret the amplitude and frequency modulation techniques and perform time and frequency domain transformations.	
Course 244.2	Identify the schemes for amplitude and frequency modulation and demodulation of analog signals and compare the performance.	
Course 244.3	Characterize the influence of channel noise on analog modulated signals.	
Course 244.4	Analyse the characteristics of pulse amplitude modulation, pulse position modulation and pulse code modulation systems.	
Course 244.5	Illustration of digital formatting representations used for Multiplexers, Vocoders and Video transmission.	
	Course Outcome Statement	


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Course:	Code: AEC 21EC45 Course Name: Biology For Engineers	Faculty: Mrs. Hemalatha
	Statement: On the completion of the course, the students will be able to:	
Course 245.1	Elucidate the basic biological concepts via relevant industrial applications and case studies.	
Course 245.2	Evaluate the principles of design and development, for exploring novel bioengineering projects.	
Course 245.3	Corroborate the concepts of biomimetics for specific requirements.	
Course 245.4	Think critically towards exploring innovative solutions for socially relevant problems	
Course 245.5	Exploring innovative biobased solutions for socially relevant problems	
	Course Outcome Statement	
Course:	Code: 21ECL46 Course Name: Communication Laboratory	Faculty: Mr. Vijaykumar, Mr. Nandish N
	Statement: On the completion of the course, the students will be able to:	
Course 246.1	Demonstrate the AM and FM modulation and demodulation by representing the signals in time and frequency domain.	
Course 246.2	Design and test the sampling, Multiplexing and PAM with relevant circuits.	
Course 246.3	Demonstrate the basic circuitry and operations used in AM and FM receivers.	
Course 246.4	Illustrate the operation of PCM and delta modulations for different input conditions.	
Course 246.5	Simulate the sampling theorem, QPSK and BPSK using MATLAB.	
	Course Outcome Statement	
Course:	Code: 21ECL47 Course Name: Embedded C Basics	Faculty: Dr. Harish S
	Statement: On the completion of the course, the students will be able to:	
Course 247.1	Write C programs in 8051 for solving simple problems that manipulate input data using different instructions of 8051 C.	
Course 247.2	Develop testing and experimental procedures on 8051 Microcontroller,	
Course 247.3	Develop programs for 8051 Microcontroller to implement real world problems.	
Course 247.4	Analyze the operation under different cases for microcontroller programs.	

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Course 247.5	Design and Develop Mini projects	
Course Outcome Statement		
Course:	Code: 18ES51 Course Name: TIME	Faculty: Mrs. Divya ML, Mr. Suchindra K R
Statement: On the completion of the course, the students will be able to:		
Course 351.1	Interpret the fundamental concepts of Management and Entrepreneurship and opportunities in order to setup a business	
Course 351.2	Describe the functions of Managers, Entrepreneurs and their social responsibilities	
Course 351.3	Interpret the components in developing a business plan	
Course 351.4	Understand the Management and Entrepreneurship to setup a business ideas.	
Course 351.5	Get awareness about various sources of funding and institutions supporting entrepreneurs	
Course Outcome Statement		
Course:	Code: 18EC52 Course Name: Digital Signal Processing	Faculty: Mrs. Lavanya vaishanvi DA
Statement: On the completion of the course, the students will be able to:		
Course 352.1	Analysis of real and complex discrete time signals using DFT	
Course 352.2	Utilize the FFT algorithms for the computation of DFT and linear filtering approach	
Course 352.3	Design and realize FIR and IIR digital filters	
Course 352.4	Comprehend the DSP processor architecture	
Course 352.5	Simulate the some of basic filters and signals using MATLAB/python	
Course Outcome Statement		
Course:	Code: 18EC53 Course Name: Principles of Communication Systems	Faculty: Dr. Anilkumar C
Statement: On the completion of the course, the students will be able to:		
Course 353.1	Understand the amplitude modulation and frequency modulation techniques.	
Course 353.2	Generation and demodulation schemes for amplitude modulation and frequency modulation.	

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Course 353.3	Analyze the effect of noise on amplitude modulation and frequency modulation.	
Course 353.4	Generation and demodulation of pulse amplitude modulation, pulse position modulation and pulse code modulation systems.	
Course 353.5	Applying digital formatting representations used for Multiplexers, Vocoders and Video transmission.	
Course Outcome Statement		
Course:	Code: 18EC54 Course Name: Information Theory and Coding	Faculty: Dr. Harish S
Statement: On the completion of the course, the students will be able to:		
Course 354.1	Apply concept of Dependent & Independent Source, measure of information, Entropy, Rate of Information and Order of a source	
Course 354.2	Apply Shannon Encoding, Shannon Fano, and Huffman Encoding Algorithms for source coding	
Course 354.3	Model the continuous and discrete communication channels using input, output and joint probabilities	
Course 354.4	Design the encoding and decoding circuit for linear block codes, cyclic block codes and convolution codes	
Course 354.5	Design the encoding and decoding circuit for convolution codes	
Course Outcome Statement		
Course:	Code: 18EC55 Course Name: Electromagnetic Waves	Faculty: Mr. Vijaykumar
Statement: On the completion of the course, the students will be able to:		
Course 355.1	Evaluate problems on electrostatic force, electric field duetopoint, linear, volume charges by applying conventional methods and charge in a volume.	
Course 355.2	Apply Gauss law to evaluate Electric fields due to different charge distributions and Volume Charge distribution by using Divergence Theorem.	
Course 355.3	Determine potential and energy with respect to point charge and capacitance using Laplace equation and Apply Biot-Savart's and Ampere's laws for evaluating Magnetic field for different current configurations	
Course 355.4	Calculate magnetic force, potential energy and Magnetization with respect tomagnetic materials and voltage induced in electric circuits.	
Course 355.5	Apply Maxwell's equations for time varying fields, EM waves in free space and conductors and Evaluate power associated with EM waves using Poynting theorem	
Course Outcome Statement		
Course:	Code: 18EC56 Course Name: Verilog HDL	Faculty: Mr. Nandish N
Statement: On the completion of the course, the students will be able to:		

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Course 356.1	Identify the suitable Abstraction level for a particular digital design.
Course 356.2	Design and verify the functionality of digital circuit/system using test benches.
Course 356.3	Model the given digital system in gate, dataflow (RTL), behavioral techniques.
Course 356.4	Model the digital system using Verilog tasks, functions and directives.
Course 356.5	Perform timing and delay Simulation and Interpret the various constructs in logic synthesis.

Course Outcome Statement

Course:	Code: 18EC57	Faculty: Dr. Madhuchandra G
	Course Name: Digital Signal Processing LAB	
Statement: On the completion of the course, the students will be able to:		
Course 357.1	Comprehend the concepts of analog to digital conversion of signals and frequency domain sampling of signals	
Course 357.2	Model of discrete time signals and systems and verification of its properties and results	
Course 357.3	Realize the digital filters using a simulation tool and analyze the response of the filter for an audio signal.	
Course 357.4	Implement discrete computations using DSP processor and verify the results	

Course Outcome Statement

Course:	Code: 18ECL58,	Faculty: Mr. Nnadesh N
	Course Name: HDL laboratory	
Statement: On the completion of the course, the students will be able to:		
Course 358.1	Write the Verilog programs to simulate Combinational circuits in Dataflow, Behavioral and Gate level Abstractions.	
Course 358.2	Describe sequential circuits like flip flops and counters in Behavioral description and obtain simulation waveforms on Xilinx ISE simulator.	
Course 358.3	Synthesize Combinational and Sequential circuits on programmable Ic CPLD xc9572. and test the hardware.	
Course 358.4	Interface the hardware to the programmable chips CPLD xc9572 and obtain the required output.	

Course Outcome Statement

Course:	Code: 18EC61	Faculty: Mr. VijayKumar
	Course Name: Digital Communication	
Statement: On the completion of the course, the students will be able to:		

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Course 361.1	Associate and apply the concepts of Bandpass sampling to well specified signals and channels.
Course 361.2	Analyze and compute performance parameters and transfer rates for low pass and bandpass symbol under ideal and corrupted non band limited channels.
Course 361.3	Test and validate symbol processing and performance parameters at the receiver under ideal and corrupted bandlimited channels.
Course 361.4	Demonstrate that bandpass signals subjected to corruption and distortion in a bandlimited channel can be processed at the receiver to meet specified performance criteria

Course Outcome Statement

Course:	Code: 18EC62	Faculty: Dr. Shivaprasad K M
	Course Name: Embedded Systems	
Statement: On the completion of the course, the students will be able to:		
Course 362.1	Describe the architectural features and instructions of 32 bit microcontroller ARM Cortex M3.	
Course 362.2	Apply the knowledge gained for Programming ARM Cortex M3 for different applications.	
Course 362.3	Interpret the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.	
Course 362.4	Develop the hardware software co-design and firmware design approaches.	
Course 362.5	Explain the need of real time operating system for embedded system applications.	

Course Outcome Statement

Course:	Code: 18EC63	Faculty: Mr. Nandish N
	Course Name: Microwave & Antenna	
Statement: On the completion of the course, the students will be able to:		
Course 363.1	Derive and apply the steady state transmission line equations to the design of distributed circuit	
Course 363.2	Analyze various parameters related to microwave transmission lines and waveguides	
Course 363.3	Demonstrate the working of various active & passive microwave devices & compute performance parameters	
Course 363.4	Analyse and compute antenna parameters for different applications	

Course Outcome Statement

Course:	Code: 18ECL66	Faculty: Mrs. Lavanya Vaisnavi DA
	Course Name: Embedded System Lab	
Statement: On the completion of the course, the students will be able to:		

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Course 366.1	Write a assembly programs to solve the given simple problems by exploring the instruction set of ARM cortex M3
Course 366.2	Write a embedded C program to interface simple external devices with Cortex M3 based microcontroller and control their operation.
Course 366.3	Execute the experiment using embedded C and Keil software, Observe and analyze the result.
Course 366.4	Communicate the outcomes and details of the experiment conducted

Course Outcome Statement

Course:	Code: 18ECL67	Faculty: Mr. Vijaykumar
	Course Name: Communication Lab	
Statement: On the completion of the course, the students will be able to:		

Course 367.1	C Design and test the communication circuits for different analog modulation schemes
Course 367.2	Design and demonstrate the digital modulation techniques
Course 367.3	Demonstrate and measure the wave propagation in microstrip antennas
Course 367.4	Characteristics of microstrip devices and measurement of its parameters

Course Outcome Statement

Course:	Code: 18ECMP68	Faculty: Mrs. Lavanya Vaisnavi DA
	Course Name: Mini project	
Statement: On the completion of the course, the students will be able to:		

Course 368.1	Develop the skills to summarize the contributions and limitations of the existing literature
Course 368.2	Implement, and critically analyze the scientific results obtained by hardware/software implementation of the work
Course 368.3	Develop the skills to work as a team and coordinate to solve the technical problems
Course 368.4	Develop the presentation skills to explain the technical contents with proper time management
Course 368.5	Document the work with requirements on structure, format, and language usage

Course Outcome Statement

Course:	Code: 18EC71	Faculty: Dr. Anilkumar c
	Course Name: Computer Networks	
Statement: On the completion of the course, the		

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	students will be able to:	
Course 471.1	Outline the concepts of Data Communication	
Course 471.2	Illustrate the various networking architectures	
Course 471.3	Identify the protocols and services of different layers	
Course 471.4	Analyze a simple network and measure its parameters	
	Course Outcome Statement	
Course:	Code: 18EC72	Faculty: Mrs. Lavanya Vaisnavi DA
	Course Name: VLSI Design	
	Statement: On the completion of the course, the students will be able to:	
Course 472.1	Demonstrate the working of MOS transistor theory.	
Course 472.2	Interpret CMOS fabrication flow, physical design aspects, technology scaling and delay models	
Course 472.3	Demonstrate ability to design combinational, sequential and dynamic logic circuit.	
Course 472.4	Interpret memory elements along with timing considerations.	
Course 472.5	Interpret testing & testability issues in VLSI Design	
	Course Outcome Statement	
Course:	Code: 18ECL76	Faculty: Dr. Harish S
	Course Name: Computer Networks Lab	
	Statement: On the completion of the course, the students will be able to:	
Course 476.1	Choose suitable tools to model a network	
Course 476.2	Use the network simulator for learning and practice of networking algorithm	
Course 476.3	Illustrate the operation of network protocols and algorithms using C Programming	
Course 476.4	Simulate the network with different configurations to measure the performance parameters	
Course 476.5	Implement the data link and routing protocols using C programming	
	Course Outcome Statement	



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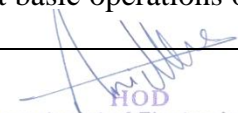
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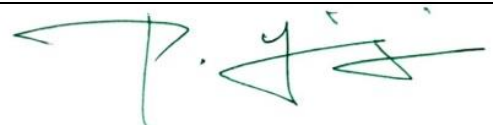
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Course:	Code: 18ECL77 Course Name: VLSI Design LAB	Faculty: Mrs. Lavanya Vaisnavi DA
	Statement: On the completion of the course, the students will be able to:	
Course 477.1	Design and simulate combinational and sequential digital circuits using Verilog HDL and analysis using EDA tool	
Course 477.2	Perform ASIC design flow and interpret the process of synthesis, synthesis constraints and evaluating the synthesis reports to obtain optimum gate level net list	
Course 477.3	Design and simulate basic CMOS circuits like inverter, common source amplifier and differential amplifiers	
Course 477.4	Perform RTL-GDSII flow and analyses the stages in ASIC design.	
	Course Outcome Statement	
Course:	Code: 18ECP78 Course Name: Project Phase-1	Faculty: Dr. Harish S, Dr. Anilkumar c
	Statement: On the completion of the course, the students will be able to:	
Course 478.1	Develop the skills to summarize the contributions and limitations of the existing literature	
Course 478.2	Apply relevant knowledge and skills, which are acquired within the technical area to formulate a meaningful problem.	
Course 478.3	Design ,develop and implement the concepts based on the problem identified	
Course 478.4	Develop teamwork and presentation skills to explain the technical contents with proper time management	
Course 478.5	Document the work with requirements on structure, format, and language usage	
	Course Outcome Statement	
Course:	Code: 18EC81 Course Name: Wireless Cellular Communication	Faculty: Mrs. Shilpakala V
	Statement: On the completion of the course, the students will be able to:	
Course 481.1	Explain concepts of propagation mechanisms like Reflection, Diffraction, Scattering in wireless channels.	
Course 481.2	Develop a scheme for idle mode, call set up, call progress handling and call tear down in a GSM cellular network.	
Course 481.3	Develop a scheme for idle mode, call set up, call progress handling and call tear down in a CDMA cellular network.	
Course 481.4	Interpret basic operations of Air interface in a LTE 4G system	


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Course Outcome Statement	
Course:	Code: 18EC821 Course Name: Network Security
	Faculty: Dr. Anilkumar C
	Statement: On the completion of the course, the students will be able to:
Course 482.1	Analyze security services and mechanism and explain security concepts
Course 482.2	Interpret concept of transport level security and secure socket layer
Course 482.3	Analyze security in internet protocol security
Course 482.4	Determine the characteristics of intruders, intrusion detection and malicious software
Course 482.5	Classify firewalls, firewalls characteristics, biasing and configurations
Course Outcome Statement	
Course:	Code: 18ECP83 Course Name: Project Phse-2
	Faculty: Dr. Anilkumar c, Dr. Harish S
	Statement: On the completion of the course, the students will be able to:
Course 483.1	Develop the skills to summarize the contributions and limitations of the existing literature
Course 483.2	Implement, and critically analyze the scientific results obtained by hardware/software implementation of the work
Course 483.3	Develop the skills to work as a team and coordinate to solve the technical problems
Course 483.4	Develop the presentation skills to explain the technical contents with proper time management
Course 483.5	Document the work with requirements on structure, format, and language usage
Course Outcome Statement	
Course:	Code: 18ECS84 Course Name: Technical Seminar
	Faculty: Dr. Anilkumar C, Dr. Harish S
	Statement: On the completion of the course, the students will be able to:
Course 484.1	Identify/Research the literature and select a topic in the contemporary areas/trends/developments in Electronics and Communication Engg. fields
Course 484.2	Develop the skills to work as a team and coordinate to prepare a presentation on the topic selected (Guide Interaction)
Course 484.3	Communicate effectively to diverse audience using a suitable engineering/IT tool.

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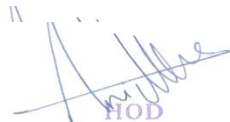
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Course 484.4	Organize and prepare effective technical reports
Course Outcome Statement	
Course:	Code:18ECI85 Course Name: Internship
	Faculty: Dr. Anilkumar C, Dr. Harish S
	Statement: On the completion of the course, the students will be able to:
Course 485.1	Assist the student's development of employer-valued skills such as teamwork, communications and attention to detail.
Course 485.2	Expose the student to the environment and expectations of performance on the part of engineers in professional engineering practice, private/public companies or government entities.
Course 485.3	Enhance and/or expand the student's knowledge of a particular area(s) of engineering
Course 485.4	Expose the student to professional role models and will provide the students with support of the internship and provide an example of the behaviors expected in the intern's workplace.


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