R L JALAPPA INSTITUTE OF TECHNOLOGY

(Affilated to Visvesvaraya Technological University, & Approved by AICTE)

Doddaballapura - 561203, Bangalore Rural District

DEPARTMENT OF XXXXX

Faculty Name	Lavanya Vaishnavi DA	
Designation	Assistant Professor	
Area of Interest	Wireless Communication, VLSI design, Verilog and HDL Coding, Machine Learning and python coding	
Email ID	lavanyavaishnavida@rljit.in	
Research Identity	Orcid Id: 0000-0001-9303-7619 Scopus Id: Google Scholar Id: https://scholar.google.com/citations?hl=en &user=lbBTg5gAAAAJ Research gate Id: https://www.researchgate.net/profile/Lavanya-Vaishnavi-Da Vidwan Id: 353014	

EDUCATION:

Currently enrolled for PhD under the guidance of Dr. Anil Kumar C, In the research and Development centre of R L Jalappa Institute of Technology.

I completed my Master of Technology in Reva Institute of Management in the emerging field of VLSI and embedded systems. I secured about 80% in my studies.

I completed my Bachelor of Engineering in RL Jalappa Institute of Technology, in Electronics and Communication Engineering with score of 72%.

I completed my schooling in Swami Vivekananda English High School in Doddaballapur.

TECHNICAL SKILLS:

I have completed Udemy courses in Python, 5G communication and basics of communication systems, and OpenCV using Python.

I'm well trained in Cadence tool, analog design and digital design.

On my interest, I have learned the basics of Labview.

Multisim, Pspice and Xilinx are also my area of Interest.

PROFESSIONAL WORK EXPERIENCE:

Started my teaching career on 1st of February 2010, as lecturer in the esteemed institution of RL Jalappa Institute of Technology. I was promoted as assistant professor on 1st of December 2015.

PUBLICATIONS:

JOURNAL

Anil Kumar C, Chethan Venkatesh, **Lavanya Vaishnavi D A**, Harish S, "Computer vision based Hand gesture recognition system", Neuro Quantology, Jul 2022, Volume 20, Issue 7, Page 2859-2866, DOI: 10.14704/nq.2022.20.7.NQ33365

Lavanya Vaishnavi D A, Anil Kumar C, Harish S, Divya M L, " MediaPipe to Recognise the Hand Gestures", WSEAS TRANSACTIONS on SIGNAL PROCESSING Volume 18, 2022, DOI: 10.37394/232014.2022.18.19

Anil Kumar C, **Lavanya Vaishnavi D A**, "Renesas Based Smart ROBOT using Embedded System". International Journal of Research in Science, Commerce, Arts, Management and Technology, Volume 2, Issue 1, February 2021. (DOI: 10.48175/IJRSCAMT-101),

Anil Kumar C., Pradeep Kumar B.P, Venu K N, **Lavanya Vaishnavi D.A.,** "Intra Prediction Algorithm for Video Frames of H.264", 2021, Natural Volatiles and essential oils journal, Page 51-58, Volume: 8 Issue: 5.

Anil Kumar C, **Lavanya Vaishnavi D A**, "Renesas Based Smart ROBOT using Embedded System", International Journal of Research in Science, Commerce, Arts, Management and Technology, Volume 2, Issue 1, February 2021, ISSN (Online) 2582-8045

INTERNATIONAL CONFERENCE:

NATIONAL CONFERENCE:
Presented a paper in titled, "Enhancing the Security of Information using BPCS", add the International Conference on Communication and Computing bargained by the Society of Information Processing, Bengaluru and Alpha College of Engineering, Bengaluru in the year 2015.

Presented paper, " design and implementation of colour Conversion Module RGB to Y, CB, CR and vice versa" with paper ID. NAREC-SP-56 in 2 day "National Conference on Advanced Research in Electronics and Communication Engineering 2014" Organised by RL Jalappa Institute of Technology.

Presented paper entitled, " an FPGA chip identification generator using configurable ring Oscillator." In national conference of E Innovations 2013 organised by Department of Electronics and Communication Engineering, Sridevi Institute of Engineering and Technology, Tumkur in the year 2013.

Presented paper "improvement of frequency stability for ring oscillator" in 9th national Women's Science Congress organized in RL Jalappa Institute of Technology in association with Women Signs Congress, on 6th and 7th November of 2016.

Presented a paper on a cryptographic algorithm based on linear feedback shift register in a national conference on emerging Trends in electronics and Communication Engineering held on 22nd of April 2016, organized by Department of Electronics and Communication Engineering in association with

IETE, Bengaluru.

TEXT BOOK AUTHORED:
Fundamentals of Digital Circuit And Design, Year: 2022, ISBN: 978-93-5625-147-2, Scientific International Publishing House

PATENTS:

FUNDED PROJECTS:
"Maze Solver Robot ", funded by KSCST in the year 2020.

INVITED TALKS:
- Gave one day lecture on introduction to VHDL programming held on 19th one, 2022 for Department of Computer Science and Engineering of SJC Institute of Technology, CHICKBALLAPUR.
- Gave an expert talk on "VLSI design" organized by IEEE Bangalore section on 26th of May 2020.

FDP/WORKSHOP ATTENDED:
- Attended one week hands on faculty development programme on "Labview for engineering applications" organized by Nagarjuna College of Engineering and Technology during 6th to 10th of January 2020.
- Attended "experience learning on electronic devices and digital system design" organized by Department of Electronics and Communication Engineering of Sjb Institute of Technology. During the 15th to 17th of July 2019.
- Attended "analogue and digital IC design using cadence tool" organized by Department of Electronics and Communication Engineering, Nagarjuna College of Engineering and Technology in association with N Tuple Technologies Pvt Lmtd on 12th of March to 14th of March 2019.
- Attended "Matlab Primer" held on RL Jalappa Institute of Technology on 10th and 11th of March 2016.
- Attended hands on "workshop on VLSI design using Cadence tool suite", Organised buy Sambhram Institute of Technology on 28th to 30th of July 2010.
- Attended advances in "wireless communication and networks" on 30th April 2010 organised by Department of Electronics and Communication Engineering in RL Jalappa Institute of Technology.
- Attended 5 days faculty development programme on "programming ARM processor with hands on using ESA keil Arm Cortex M3 Evolution board", organized buy BMS Institute of Technology and Management in association with Electro systems associate. From 11th to 15th of December 2017.
- Participated in a seminar on :"intellectual property rights and patent awareness programme" organized in association with Karnataka State Council for Science and Technology in Reva Institute of Technology and Management on 15th of December 2012.
- Attended 22nd ISTE state Level faculty convention organized by SJC Institute of Technology on 4th of February 2020.

CONFERENCE/FDP/WORKSHOP CONDUCTED:
- Worked as a coordinator for international conference or computer science and information technology held by Department of Information Science and Engineering in RL Jalappa Institute of Technology on 17th and 18th of September 2010.
- Worked as a coordinator for international conference on recent trends in Mechatronics, Electronics and cloud computing, held on December 22nd and 23rd of 2016, organized by RL Jalappa Institute of Technology.
- Work as a coordinator for international conference on researchers advances in integrated navigation system at RL Jalappa Institute of Technology, Bengaluru on May 6th, 2016.
- Work as a coordinator for international conference on Advances in human machine interaction organized by a RL Developer Institute of Technology on 3rd to 5th of May 2016
- Work as a coordinator for international Conference on Computing, modelling and security

organized by RL Jalappa Institute of Technology on February 11th to February 13th of 2016.

UNIVERSITY RESPONSIBILITIES:
- Contributed towards setting university question paper for Visvesvaraya Technological university and autonomous Colleges.
- Evaluating the performance of students as an external lab examiner in affiliated VTU colleges and autonomous VTU colleges.

OTHER RESPONSIBILITES:
COLLEGE LEVEL:
- Work does alumni coordinator.
- Editorial member of SMRUTHI saw for 5 continuous years.
- Work for SANTHASA, a cultural fest of college.
- Coordinated X-ibit, a technical event organized in the college.
- Organized a different other college level events.

DEPARTMENT LEVEL:
- e-Suddi coordinator form the Department
- CIE coordinator
- Local guardian Coordinator
- Time Table Coordinator
- Alumni Representative

PROFESSIONAL MEMBERSHIPS:
Life member of IETE.
Life member of Institute of Researchers.

HONORS/AWARDS:
Young Researcher Award by Institute of Researchers in the year 2021
Distinguished Professor Award in the year 2021 by Institute of Researchers.

OTHER SIGNIFICANT ACHIEVEMENTS (if any):
Gate qualified in the year 2023 with a valid GATE score of 358, and All India ranking of the paper as 6951.

CONTACT DETAILS:
 Mobile and WhatsApp: +91 9844 315 171