




# R L JALAPPA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, & Approved by AICTE)

Doddaballapura - 561203, Bangalore Rural District

## DEPARTMENT OF XXXXX

<b>Faculty Name</b>	HARISH S	
<b>Designation</b>	ASSOCIATE PROFESSOR	
<b>Area of Interest</b>	Embedded Systems, Digital Circuits, Image Processing, Bio-Medical Image Processing, Communication Systems, and Artificial Intelligence	
<b>Email ID</b>	<a href="mailto:harishs@rljit.in">harishs@rljit.in</a> <a href="mailto:harishsrinivasaiah@gmail.com">harishsrinivasaiah@gmail.com</a>	
<b>Research Identity</b>	Orcid Id: <a href="https://orcid.org/0000-0003-3994-4300">https://orcid.org/0000-0003-3994-4300</a> Scopus Id: <a href="https://scopus.org/57202653236">57202653236</a> Google Scholar Id: <a href="https://scholar.google.com/citations?hl=en&amp;user=jWc78P8AAAAJ">https://scholar.google.com/citations?hl=en&amp;user=jWc78P8AAAAJ</a> Research gate Id: <a href="https://www.researchgate.net/profile/Harish-S-6">https://www.researchgate.net/profile/Harish-S-6</a> Vidwan Id:	

### EDUCATION:

- Ph.D in Electrical Engineering from Visvesvaraya Technological University
- M.Tech., Information & Communication Systems from Visvesvaraya Technological University
- B.E., Telecommunication Engineering from Bangalore University

### TECHNICAL SKILLS:

Embedded C, C++, MatLab, CCS Studio, Keil Version-4, Keil Version-5, NS-2, NI Multisim.

### PROFESSIONAL WORK EXPERIENCE:

- Working as Associate Professor in the Department of Electronics and Communication Engineering, R. L. J. I. T., Doddaballapur – 561 203 from 01/01/2014 to till date.
- Worked as Assistant Professor in the Department of Electronics and Communication Engineering, R. L. J. I. T., Doddaballapur – 561 203 from 06/07/2012 to 31/12/2013.
- Worked as Assistant Professor in the Department of Electronics and Communication Engineering, C. I. T., Gubbi – 572 216 from 01/08/2008 to 30/06/2012.
- Worked as Senior Lecturer in the Department of Electronics and Communication Engineering, C. I. T., Gubbi – 572 216 from 01/08/2007 to 31/07/2008.
- Worked as Lecturer in the Department of Electronics and Communication Engineering, C. I. T., Gubbi – 572 216 from 22/09/2004 to 31/07/2007.

## PUBLICATIONS:

### JOURNALS:

1. S. Harish, C. Anil Kumar, Lakshmi Shrinivasan, S. Rohith, Belete Tessema Asfaw, "Algorithm for Recognition of Movement of Objects in a Video Surveillance System Using a Neural Network", Journal of Engineering, vol. 2022, Article ID 8216221, 4 pages, 2022. <https://doi.org/10.1155/2022/8216221>
2. C. Anil Kumar, S. Harish, Prabha Ravi, Murthy SVN, B. P. Pradeep Kumar, V. Mohanavel, Nouf M. Alyami, S. Shanmuga Priya, Amare Kebede Asfaw, "Lung Cancer Prediction from Text Datasets Using Machine Learning", BioMed Research International, vol. 2022, Article ID 6254177, 10 pages, 2022. <https://doi.org/10.1155/2022/6254177>
3. Anil Kumar C, Chethan Venkatesh, Lavanya Vaishnavi D A, Harish S , "Computer vision based Hand gesture recognition system", NeuroQuantology An Interdisciplinary Journal of Neuroscience and Quantum Physics, ISSN 1303 5150. DOI: [10.14704/nq.2022.20.7.NQ33365](https://doi.org/10.14704/nq.2022.20.7.NQ33365)
4. S., Harish A., & Lavanya & C., Anil & L., Divya. (2022), "MediaPipe to Recognise the Hand Gestures". WSEAS TRANSACTIONS ON SIGNAL PROCESSING. 18. 134-139. DOI:[10.37394/232014.2022.18.19](https://doi.org/10.37394/232014.2022.18.19).
5. S, Harish & Ahammed, G.F. (2019). Integrated Modelling Approach for Enhancing Brain MRI with Flexible Pre-Processing Capability. International Journal of Electrical and Computer Engineering (IJECE). <http://doi.org/10.11591/ijece.v9i4.pp2416-2424>
6. S. Harish & Ahammed, G.F. (2020). An optimized approach for extensive segmentation and classification of brain MRI. International Journal of Electrical and Computer Engineering (IJECE). <http://doi.org/10.11591/ijece.v10i3.pp2392-2401>
7. Dr. Harish S., (2023). Classroom Attendance using Face Detection and Facial Organs. International Journal of Advanced Research in Science, Communication and Technology (IJARSCT). DOI: [10.48175/IJARSCT-8608](https://doi.org/10.48175/IJARSCT-8608)

### INTERNATIONAL CONFERENCE:

1. S. Harish, G. F. A. Ahammed and R. Banu, "An extensive research survey on brain MRI enhancement, segmentation and classification," 2017 International Conference on Electrical, Electronics, Communication, Computer, and Optimization Techniques (ICEECCOT), Mysuru, 2017, pp. 1-8, doi: [10.1109/ICEECCOT.2017.8284578](https://doi.org/10.1109/ICEECCOT.2017.8284578).
2. Harish S., Ali Ahammed G.F. (2019) BrainMRI Enhancement as a Pre-processing: An Evaluation Framework Using Optimal Gamma, Homographic and DWT Based Methods. In: Silhavy R., Silhavy P., Prokopova Z. (eds) Intelligent Systems in Cybernetics and Automation Control

Theory. CoMeSySo 2018. Advances in Intelligent Systems and Computing, vol 860. Springer, Cham. [https://doi.org/10.1007/978-3-030-00184-1\\_27](https://doi.org/10.1007/978-3-030-00184-1_27)

3. Harish S., Ali Ahammed G.F. (2019) Comprehensive Framework for Classification of Abnormalities in Brain MRI Using Neural Network. In: Silhavy R., Silhavy P., Prokopova Z. (eds) Computational Statistics and Mathematical Modeling Methods in Intelligent Systems. CoMeSySo 2019 2019. Advances in Intelligent Systems and Computing, vol 1047. Springer, Cham. [https://doi.org/10.1007/978-3-030-31362-3\\_8](https://doi.org/10.1007/978-3-030-31362-3_8)

#### NATIONAL CONFERENCE:

1. Harish S., “Porting Android to ARM for Wireless Data Encryption and Decryption” in National Conference on “Networking Embedded and Wireless Systems” on 04/08/2012, conducted by BMSCE, Bangalore.

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#### BOOK CHAPTERS:

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#### TEXT BOOK AUTHORED:

1. Digital Electronics-I, A text book for first semester Diploma in ECE, EI & C and second semester Diploma in CSE and ISE, 2011, Rasha Publications
2. Fundamentals of Digital Circuit And Design, 2022, ISBN:978-93-5625-147-2, Scientific International Publishing House.
3. Internet of Things, 2023, ISBN:

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#### PATENTS:

**Energy Efficient Wireless Communication for IoT Enabled Greenhouses (Applied)**

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#### FUNDED PROJECTS:

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#### INVITED TALKS:

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#### FDP/WORKSHOP ATTENDED:

- Have participated and successfully completed Nine (9) days Faculty Development Programme (FDP) on “Collaborative Research Practices in Modern Era” from 13/02/2023 to 21/03/2023 and rewarded with A grade.
- Have participated in “Faculty Conclave” held on 1<sup>st</sup> May 2022 organized by AP/MTT-S Bangalore Joint Chapter.
- Have participated and successfully completed One Week Online AICTE – VTU Joint Teachers Training Programme on “An Overview of Teaching Techniques in Data Science and Analytics” between 25<sup>th</sup> & 29<sup>th</sup> April 2022 Organized by VTU Human Resource Development Centre (VTU - HRDC), Centre for PG Studies, VIAT, Muddenahalli, Chikkaballapur (Dist.) – 562 101

- Have participated and successfully completed One Week Online AICTE – VTU Joint Teachers Training Programme on “An Overview of Teaching Techniques in Mathematics Laboratory Using Python Programming” between 21<sup>st</sup> & 25<sup>th</sup> February 2022 Organized by VTU Human Resource Development Centre (VTU - HRDC), Centre for PG Studies, VIAT, Muddenahalli, Chikkaballapur (Dist.) – 562 101
- Have participated in orientation Session on National Education Policy (Focus on Innovation and Entrepreneurship) on 22<sup>nd</sup> January, 2022 organized by SJC Institute of Technology, Chikballapur – 562 101
- Have participated in “Digital VLSI Design and Verification” organized by Department of ECE, Bangalore Institute of Technology in association with Entuple Technologies and IEEE-BIT CAS from 23<sup>rd</sup> to 27<sup>th</sup> August 2021.
- Have guided the project “Desktop Assistant”, which was presented in the Symposium cum Project Exhibition on Recent Advances in Engineering Science organized by S J C Institute of Technology In association with Karnataka Science and Technology Academy on 19<sup>th</sup> July 2021.
- Have participated in 22<sup>nd</sup> International Webinar on “Achieving energy efficiency in high data rate transmission techniques for 5G systems” organized by IETE Mysuru Centre held on 03<sup>rd</sup> June, 2021.
- Have participated in a webinar on “Intellectual Property Rights (IPR) and IP Management for Startup” organized by Institution Innovation Council & Department of Electronics and Communication Engineering, S J C Institute of Technology, in association with IEEE SB SJCIT, IEEE Bangalore Section on 19<sup>th</sup> May 2021.
- Have participated in one day Hands on workshop “NI – LabVIEW and Applications” on 06/02/2017, conducted by R. L. Jalappa Institute of Technology, Doddaballapur – 561 203
- Have attended a Faculty Development Program on “Programming ARM Processor with Hands on using ESA KEIL ARM Cortex-M3 Evaluation Board”, conducted by BMSIT&M, Yelahanka, Benagalurur – 560 064 from 11-12-2017 to 15-12-2017.
- Have attended workshop on “Research Methodology & Latex”, organized by VTU e-Learning Centre, Mysuru conducted at Raja Rajeswari College of Engineering, Bengaluru from 20-12-2016 to 22-12-2016.
- Have participated in two day certification programme on “Research Methodology – As a part of capacity building for faculty members and PhD Scholars in Engineering and Management”, held at Channabasaveshwara Institute of Technology, Gubbi, Tumkur – 5720216 from 19/08/2016 to 20/08/2016.
- Have attended workshop on “Recent Trends in 4G/5G Wireless Mobile Communication”, conducted by M. S. Ramaiah Institute of Technology, Bengaluru from 11-07-2016 to 16-07-2016.
- A paper titled “Design and performance analysis of brain tumors detection by region based segmentation and ANN classification” is presented in 2nd International Conference

on Nanomaterials and Technologies (CNT 2014) on 17-18 October 2014 Organized by R&D Centre, Department of Electrical and Electronics Engineering, Vardhaman College of Engineering, Hyderabad.

- Have attended AICTE sponsored two weeks FDP on “Real World DSP” conducted by Channabasaveshwara Institute of Technology, Gubbi, Tumkur – 572 0216 from 15/07/2013 to 27/07/2013.
- Have attended National Symposium on “Research Methodologies” conducted by K. S. School of Engineering And Management, Bangalore from 12/10/2012 to 13/10/2012.
- A paper titled “Porting Android to ARM for Wireless Data Encryption and Decryption” is presented in National Conference on “Networking Embedded and Wireless Systems” on 04/08/2012, conducted by BMSCE, Bangalore.
- Have participated in symposium on “Digital Image Processing” held at Cambridge Institute of Technology, Bangalore – 36 from 09/12/2011 to 10/12/2011.
- Guided and Participated in Hobby Project Exhibition during 8<sup>th</sup> ISTE State Level Student Annual Convention/National Level Technical Symposium, Karnataka Section, organized by Channabasaveshwara Institute of Technology, Gubbi, Tumkur - 5720216, from 14-10-2011 to 15-10-2011.
- Have participated in National Level Technical Symposium in KNEW - 11 held at Channabasaveshwara Institute of Technology, Gubbi, Tumkur - 5720216, from 30-03-2011 to 31-03-2011.
- Have attended a workshop on “Renewable Energy Sources”, conducted by Channabasaveshwara Institute of Technology, Gubbi, Tumkur - 5720216 on 09-03-2010.
- Have attended a Faculty Development Program on “Linear Algebra”, conducted by Channabasaveshwara Institute of Technology, Gubbi, Tumkur - 5720216 from 31-01-2008 to 02-02-2008.
- Have attended a Faculty Development Program on “PSOC & PCB Design”, conducted by Channabasaveshwara Institute of Technology, Gubbi, Tumkur - 5720216 from 06-08-2007 to 09-08-2007.
- Have attended a Faculty Development Program on “Brain Train For Effective Teaching”, conducted by Channabasaveshwara Institute of Technology, Gubbi, Tumkur - 5720216 from 05-02-2007 to 06-02-2007.
- Have attended workshop on “ARM Processor, Architecture and Instruction set”, conducted by VTU in collaboration with ARM Embedded Technologies Pvt. Ltd., India at “Jnana Sangama”, VTU, Belgaum on 12/09/2006.
- Have attended workshop on “Embedded System Development using ARM Controllers” held at Indian Service Machine (ISM), Bangalore – 560 010 on 05/03/2006.

- Have attended workshop on “Flash Microcontroller and DSP” held at Bangalore Institute of Technology, Bangalore from 07/08/2005 to 09/08/2005.

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**CONFERENCE/FDP/WORKSHOP CONDUCTED:**

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**UNIVERSITY RESPONSIBILITIES:**

Chief Superintendent for VTU examinations, Deputy Chief Superintendent for VTU examinations, Squad Team Member for VTU Examinations, VTU Exam Invigilator, Invigilator for Karnataka Nursing Board Exams, Practical Lab Examiner.

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**OTHER RESPONSIBILITIES:****COLLEGE LEVEL:**

Reviewer for many esteemed peer reviewed journals, Resource person for the workshops.

**DEPARTMENT LEVEL:**

Test Coordinator, Time table Coordinator, Cultural Coordinator, Department Event Coordinator.

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**PROFESSIONAL MEMBERSHIPS:**

1. Life Member of Indian Society for Technical Education (ISTE)  
Membership ID: LM46050
2. Member of Indian Institute of Engineers (India)  
Membership ID: M-1551773
3. Life member of Institute of Researchers  
Membership ID: LM082101
4. Fellow of Institution of Electronics & Telecommunication Engineers (IETE)  
Membership No: F-502710

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**HONORS/AWARDS:**

- InRes Research Excellence Award-2021
- InRes C V Raman Award-2021
- InRes Distinguished Professor Award-2021

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**OTHER SIGNIFICANT ACHIEVEMENTS (if any):-**

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**CONTACT DETAILS:**

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Tubugerepet, Near L. V. Hospital,  
Doddaballapur – 561 203  
Bengaluru Rural

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R. L. J. I.T.  
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